Department of Electronics and Telecommunication Engg Centre Of Excellence (Thinking Systems for Signal and Image Processing)

No. E&TC/COE(TSSIP)/JRF advt/ Date : **‡G**/10/25

Submitted:

Sub

: Extension for last date of submission of application for recruitment of Junior Research Fellow (JRF) for the Centre,

Of Excellence (TSSIP) till 27/10/25

Ref: 1) महाराष्ट्रशासन उच्च व तंत्रशिक्षण विभाग शासन शुध्दीपत्रक क्र. तवायो-१४२३/प्र.क्र. ८३/२३/तांशि-३,

दिनांक: जुलै०७, २०२३

2) Minutes of meeting of Institute Level Committee, dated:, 20/09/2025

As per the Ref (1) above , the Centre Of Excellence (TSSIP) has invited applications for recruitment of Junior Research Fellow (JRF)/Research Scholars till 20/10/2025. As there have been no applications received till date , hence, it is requested to provide an extension for the last date of receiving applications till 27/10/25, 5.00 pm. The revised schedule for the recruitment of the JRF shall be as follows:

Duration	Activity	Coordinator
10 th Oct.2025	Publication of the Advt	PI/Co-PI
27 th Oct. 2025	Last date to receive	Scrutiny
	applications	Committee
28/10/2025	Interview of the eligible	Interview
	candidates	Committee
01 Nov.2025	On -boarding of the	PI/Co-PI
	Research Scholars/JRF	

The JRF candidates shall be recruited on temporary basis for a period of 02 years, which may be further extended depending on the publications/performance of the candidate. The JRF candidates shall be paid a stipend of Rs.30000/- per month. The JRF candidates shall be considered for the registration for PhD Research work under Dr. BAM University as per the existing rules and regulation of admission to PhD program of Dr.BAM University. The candidature of such JRF candidates may also be converted as the Senior Research Fellow (SRF) candidates. The format of application for the post of JRF is attached herewith.

Kindly permit to provide extension for receiving applications for the process of recruitment of JRF, as detailed above.

To The Principal, Govt. College of Engineering Aurangabad Chh.Sambhajinagar (Dr. S.R. Hirekhan)
PI, COE (TSSIP) and Head, E&TC
Head of the Dept.
Lept. of Electronics & Telecom. Engg.

Govt. College of Engineering

Aurangabad (M.S.) India